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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/085,929	02/28/2002	Keith W. Holt	01-975	4364
24319	7590 11/03/2004		EXAMINER	
LSI LOGIC CORPORATION			LAMARRE, GUY J	
1621 BARBEI	R LANE			
MS: D-106			ART UNIT	Paper Number
MILPITAS, CA 95035			2133	
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DATE MAILED: 11/03/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

Application No. Applicanting Application No. Applicanting HOLT, KEITH W.	•		N	1				
Examiner Guy J. Lamarre, P.E. 2133		Application No.	Applicant(s)					
Cay J. Lamarre, P.E. 2133 Period for Reply		10/085,929	HOLT, KEITH W.					
The MALIUNG DATE of this communication appears on the cover sheet with the correspondence address — Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE ② MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. Extensions of the may be available used the prevention of 30°CR1.13(4). In an event, however, may a reply be timely filled. 1 this period for reply specified above is less than thirty (30) days, a reply within the statistory minimum of thirty (30) days will be considered drinely. 1 this period for reply specified above is less than thirty (30) days, a reply within the statistory minimum of thirty (30) days will be considered drinely. 1 Hot period for reply specified above is less than thirty (30) days, a reply within the statistory minimum of thirty (30) days will be considered drinely. 1 Hot period for reply specified above is less than thirty (30) days, a reply within the statistory minimum of thirty (30) days will be considered drinely. 1 Hot period for reply specified above is less than thirty days and reply special will be secured and the state of the commendation. 1 Hot period for reply specified doesn't have desired and the state of the commendation. 2 Hot period for reply specified above is less than thirty day days and velocity of the become and the state of the commendation. 1 May be provided the state of the state of the state of the state of the commendation. 2 In Section 1 First Plan 1 Hot period of the state of t	Office Action Summary	Examiner	Art Unit					
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🗖	Attachment(s)							
	1) Notice of References Cited (PTO-892)							
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) Notice of Informal Patent Application (PTO-152) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)		· —	Patent Application (PTO-152)					

DETAILED ACTION

1. Pursuant to 35 USC 131, Claims 1-18 are presented for examination.

Specification

2. The disclosure is objected to because para. 6 recites: 'an all' instead of 'on all,' and algorithm 300 as described in para 23 is not seen in the referred figure. Specification to be amended accordingly. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3.1 Claims 1-18 are rejected under 35 U.S.C. 102(b) as being anticipated by Iwatani (US Patent No. 6,023,780).

Iwatani anticipates the claimed invention because disclosed is means to access data for detecting drive errors wherein plural drives are congregated into a single disk configuration with means to generate metadata, means to distribute along with data address/location tagging means via temporal/spatial coordinate means to keep track such data and metadata through plural RAID levels, and means to compare such data and metadata to determine and correct disk anomalies.

Iwatani teaches, in Figs. 1-17 and related description, a disc array apparatus which 'comprises a section generating, when data is stored in the data memory device and first redundancy information is stored in the first redundancy information memory device, relevant data including second redundancy information corresponding to the first redundancy information and to be stored in the second redundancy information memory device. The disc array apparatus of the present invention further comprises a section judging, when the data is read from the plurality of data memory devices, the normality of the data on the basis of the second redundancy information stored in the second redundancy information memory device. In addition, the disc array apparatus of

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the present invention also comprises a restructuring section designating, when the data is judged to be abnormal, only one memory device among a plurality of memory devices as storing the abnormal data, and restructuring (recovering) the data stored in the designated memory device on the basis of the information stored in the remaining memory devices other than the designated memory device.'

As per claim 1, Iwatani discloses the claimed method for detecting drive anomalies in col. 2 lines 25-44, comprising: (a) verifying data is written to a media upon an occurrence of a write operation in col. 1 lines 50-54; (b) performing a data block integrity test by reading data from a single drive (in col. 1 line 55) during an occurrence of a read operation in col. 1 line 55; and (c) performing a location check by reading data from said single drive (in col. 1 lines 63-67) during said occurrence of said read operation in col. 4 line 66, wherein a data persistency verification is performed during said read operation in col. not 2 lines 1-6, 38-44, 67. As per claim 2, Iwatani discloses the claimed method as claimed in claim 1, wherein said data persistency verification determines whether data is written to said media in col. 2 line 63. As per claim 3, Iwatani discloses the claimed method as claimed in claim 1, wherein a random read performance is increased by removing the requirement of reading a form of metadata from a second drive in col. 2 line 63 - col. 3 line 3 wherein data is mirrored to thereby avoid the need for 2nd reading metadata such information as parity for drive. As per claim 4, Iwatani discloses the claimed method as claimed in claim 1, wherein said data block integrity test ensures that data has been retrieved properly in col. 3 lines 54-65 and Fig. 3: S68.

As per claim 5, Iwatani discloses the claimed method as claimed in claim 1, wherein said location check ensures that data has been retrieved from a correct physical location in col. 3 lines 54-65.

As per claim 6, Iwatani discloses the claimed method for detecting drive anomalies in col. 2